The figure illustrates the flow of data within the Embedded Processor Complex (EPC). Data enters the system at the ingress switch interface, stored in internal SRAM, and then passes through the ingress physical MAC multiplexor. From there, the data flows to the traffic management and scheduling components. Egress data is collected from external DRAM and SRAM and stored in egress SRAM before reaching the egress physical MAC multiplexor. The data then exits the system through the egress switch interface, directed to the egress data store processor. The PCI bus connects the switching fabric to the external DRAM and SRAM, allowing for data exchange between the internal and external memory resources.