| Memory Technology | Abbreviation | Purpose |
|---|---|---|
| Synchronized DRAM | SDRAM | Synchronized with CPU for lower latency |
| Quad Data Rate SRAM | QDR-SRAM | Optimized for low latency and multiple access |
| Zero Bus Turnaround SRAM | ZBT-SRAM | Optimized for low latency with random access |
| Fast Cycle RAM | FCRAM | Low cycle time optimized for block transfer |
| Double Data Rate DRAM | DDR-DRAM | Optimized for low latency |
| Reduced Latency DRAM | RLDRAM | Low cycle time and low power requirements |

**Figure 14.4** Examples of memory technologies that are optimized for specific purposes. A network processor can use a variety of memories with each memory devoted to one task.