Quantity	Component	Purpose
1	Embedded RISC processor	Control, higher layer protocols, and exceptions
8/16	Packet processing engines	I/O, basic packet processing, and packet forwarding
1+	SRAM access unit	Coordinate access to the external SRAM bus
1+	DRAM access unit	Coordinate access to the external DRAM bus
1	Media/Switch Fabric access unit	Coordinate access to the external I/O devices
1	PCI bus access unit	Coordinate access to the external PCI bus
1	Hash unit	Compute a hash function for high-speed lookup
1	Crypto unit	Compute cryptographic encoding for secure transfer
several	Onboard buses	Internal control and data transfer