Memory Type	Access Unit (bytes)	Relative Access Time	Special Features
local	4	1	accessed using the LM_ADDR registers
Scratch	4	10	synchronization via atomic read-modify-write support for IPC (rings) push/pull reflector mode
SRAM	4	14	follows QDR specification atomic operations support for queues and rings bit manipulation
DRAM	8	20	connects to: Xscale, microengines, and PCI bus master