Local CSR	Purpose
USTORE_ADDRESS	Load the microengine control store
USTORE_DATA_LOWER	Lower 20 bits of the instruction
USTORE_DATA_UPPER	Upper 12 bits of the instruction
USTORE_ERROR_STATUS	Error status bits
ALU_OUT	Debugging: allows XScale to read GPRs and transfer registers
CTX_ARB_CTL	Context arbiter control
CTX_ENABLES	Context arbiter control
CC_ENABLE	Debugging: read condition codes
CSR_CTX_POINTER	Used to modify context-specific CSRs
INDIRECT_CTX_STS	To access context-specific PC
ACTIVE_CTX_STS	Find context currently running
TIMESTAMP_HIGH	Clock (high-order bits)
TIMESTAMP_LOW	Clock (low-order bits)
PSEUDO_RANDOM_NUMBER	Random value

Figure 19.12 A few examples of local CSRs used on an Intel IXP2xxx and their purpose. Some CSRs allow the XScale to control a microengine and others are useful during debugging.