Type Of Memory	Approx. Access Time In Clock Cycles IXP2400	IXP28x0
Local Memory	1	1
Scratchpad	60	60
SRAM	150	90
DRAM	300	120

Figure 19.6 The delay that occurs when a microengine accesses memory. Delay is measured in clock cycles, which is equivalent to the number of instructions that can be executed.