Operation	Meaning
incr	Increment the specified word in memory
decr	Decrement the specified work in memory
add	Add the value in a transfer register to the specified word in memory
sub	Subtract the value in a transfer register to the specified word in memory

Figure 24.2 The atomic operations supported by the SRAM and Scratch memory controllers. The underlying hardware guarantees no interference, even if multiple microengines and the XScale attempt to perform atomic operations at the same time.