Variable	Size	Value And Meaning
exception_id	8 bits	ID of an exception handler on the XScale
exception_code	8 bits	A value passed with an exception packet
dl_next_block	8 bits	ID of next logical block for a packet
dl_buf_handle	32 bits	Buffer handle for start of the packet
dl_eop_buf_handle	32 bits	Buffer handle for end of the packet
buffer_size	16 bits	Length of the buffer containing the packet
packet_siz	16 bits	Total length of packet (across all buffers)
buffer_offset	16 bits	Offset of data from the start of the buffer
input_port	16 bits	Logical port over which the packet arrived
rx_stat	4 bits	Status flag bits (unicast, broadcast, etc.)
output_port_egress	24 bits	Port over which packet is to be sent
output_port_fabric	8 bits	Blade ID when multiple blades used
output_port_type	4 bits	Hardware type of output interface
cache_flags	4 bits	Control header caching (64 bytes of packet)
next_hop_id	32 bits	ID of the next hop for the packet
flow_id	32 bits	Flow ID for metering / policing
queue_id	16 bits	Output queue for traffic management

Figure 24.6 Variables available for a program to use with Intel's dispatch loop. The set of variables used with a given program depends on the purpose of the code.