The diagram illustrates the internal architecture of the IXP1200 chip. It features:

- SRAM and FLASH memory mapped I/O
- SDRAM access
- SRAM access
- PCI access
- Scratch memory
- PCI bus
- High-speed I/O bus
- Multiple, independent internal buses
- Optional host connection
- Serial line connection to IXP1200 chip
- Embedded RISC processor (StrongARM)
- Microengines 1 through 6

The diagram shows the various components and their interconnections within the chip's architecture.