• Sixteen microengines that operate at 1.4 GHz
• Two unidirectional sixteen-bit Low Voltage Differential Signaling data interfaces which can be configured to be a SPI-4 Phase 2 or a CSIX switching fabric interface
• Four QDR-SRAM interfaces that each have a bandwidth of 1.6 Gigabytes per second
• Three RDRAM interfaces that each have a bandwidth of 1.6 Gigabytes per second