Processor Or Unit	Purpose
Stream editor engine	Perform modifications on packet
Traffic manager engine	Police traffic and keep statistics
Traffic shaper engine	Ensure QoS parameters
Input interface	Accept packet from FPP
Packet† assembler	Store incoming packet in memory
Queue manager logic	Interface to external traffic scheduler
Output interface	External connection for outgoing packets
Configuration bus interface	Connect to external configuration bus

Figure 15.5 Examples of the functional units and processors on the Agere RSP chip. The units are interconnected.