• One embedded RISC processor
• Six programmable packet processors
• Multiple, independent onboard buses
• Processor synchronization mechanisms
• Small amount of onboard memory
• One low-speed serial line interface
• Multiple interfaces for external memories
• Multiple interfaces for external I/O buses
• A coprocessor for hash computation
• Other functional units

Figure 18.2 A list of the major facilities found on Intel’s IXP1200 network processor chip. In addition to the hardware facilities on the chip, the vendor offers a software development environment.