Local CSR	Purpose
USTORE_ADDRESS	Load the microengine control store
USTORE_DATA	Load a value into the control store
ALU_OUTPUT	Debugging: allows StrongARM to read GPRs and transfer registers
ACTIVE_CTX_STS	Determine context status
ENABLE_SRAM_JOURNALING	Debugging: place journal in SRAM
CTX_ARB_CTL	Context arbiter control
CTX_ENABLE	Debugging: enable a context
CC_ENABLE	Enable condition codes
CTX_*n*_STS	Determine context status‡
CTX_*n*_SIG_EVENTS	Determine signal status
CTX_*n*_WAKEUP_EVENTS	Determine which wakeup events are currently enabled

Figure 20.9 Local CSRs used on the Intel IXP1200 and their purpose. The StrongARM uses the first two local CSRs when it loads code into a microengine control store.