Macro	Purpose
Buf_Alloc	Allocate a packet buffer
Buf_GetData	Get the SDRAM address of a packet buffer (note: the name is misleading)
DL_Drop	Drop a packet and recycle the buffer
DL_GetBufferLength	Compute the length (in bytes) of the packet portion of a buffer
DL_GetBufferOffset	Compute the offset of packet data within a buffer
DL_GetInputPort	Obtain the input port over which the packet arrived
DL_GetOutputPort	Find the port over which the packet will be sent
DL_GetRxStat	Obtain the receive status of the packet
DL_Init	Initialize the dispatch loop macros
DL_MESink	Send a packet to next microblock group
DL_SASink	Send a packet to the StrongARM
DL_SASource	Receive a packet from the StrongARM
DL_SetAceTag	Specify the microblock that is handling a packet so the StrongARM will know
DL_SetBufferLength	Specify the length of packet data in the buffer
DL_SetBufferOffset	Specify the offset of packet data in the buffer
DL_SetExceptionCode	Specify the exception code for the StrongARM
DL_SetInputPort	Specify the port over which the packet arrived
DL_SetOutputPort	Specify the port on which the packet will be sent
DL_SetRxStat	Specify the receive status of the packet

Figure 25.2 Intel macros related to buffer management and the dispatch loop. Many of the macros access state information that is kept with each packet.